



Fig. 1

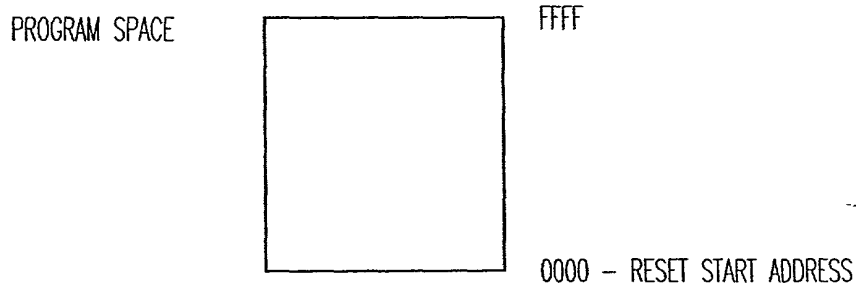
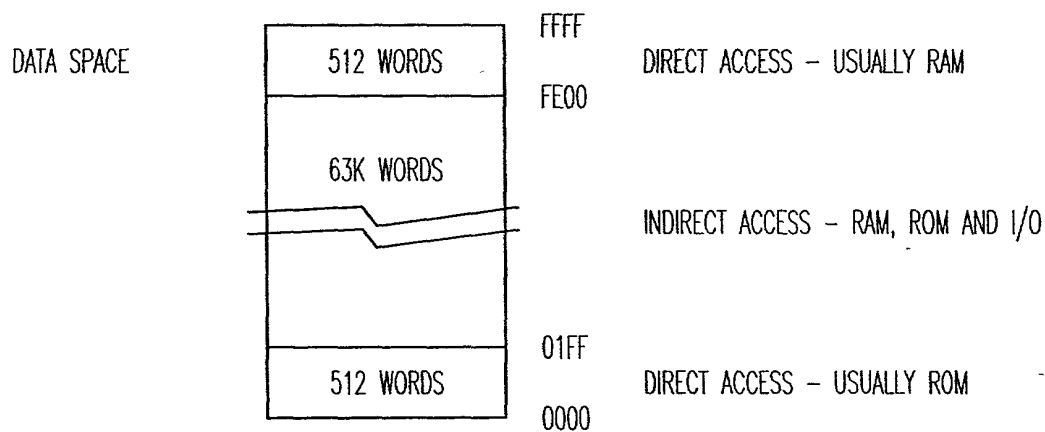
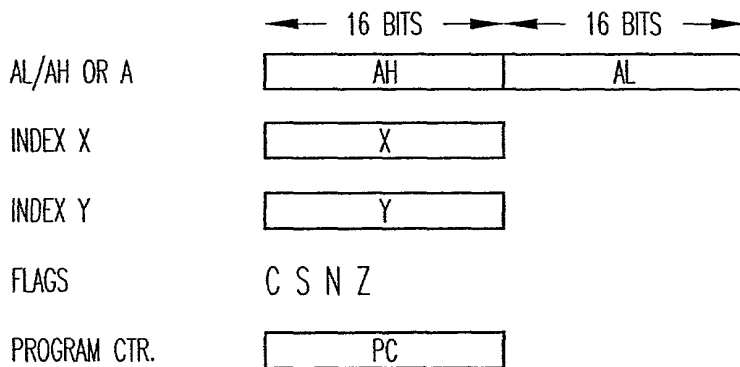
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INVENTORS NAME: Stephen J. Barlow et al.

SERIAL NO.: 08/809,498

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PROGRAMMERS MODEL



INSTRUCTION

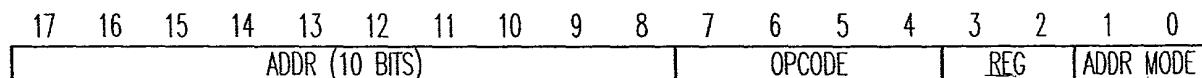


Fig.2



Fig. 3

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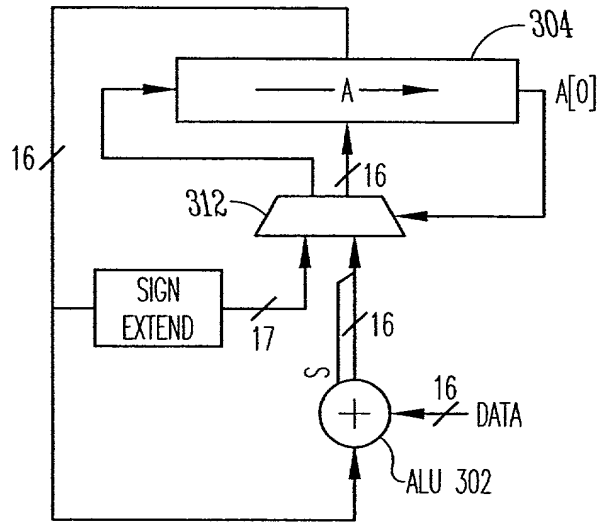


Fig.4A

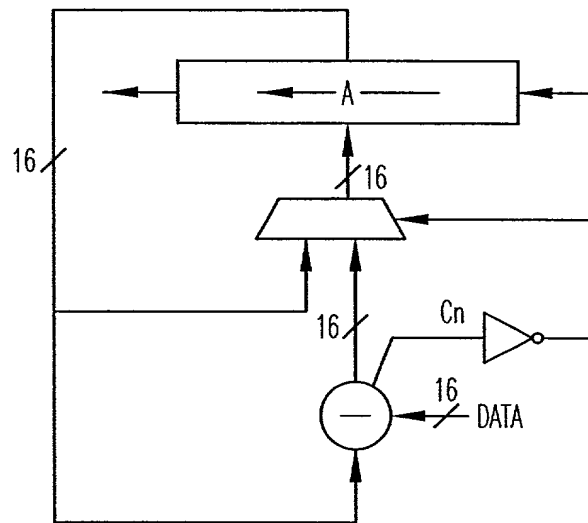


Fig.4B

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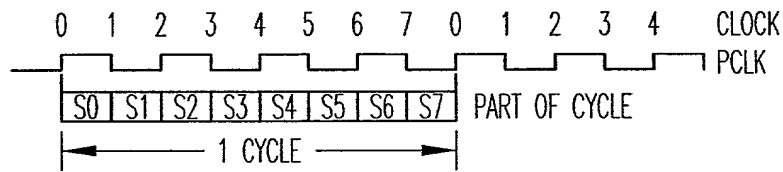


Fig.5A

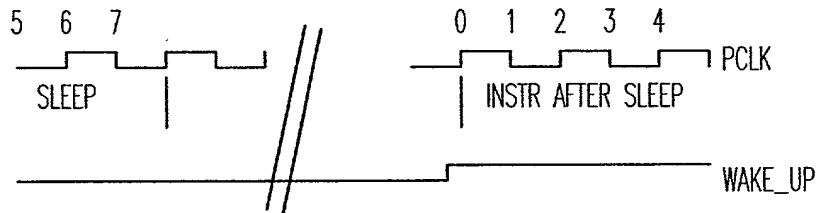


Fig.5B

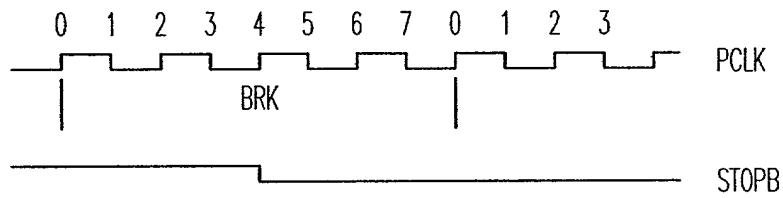


Fig.5C

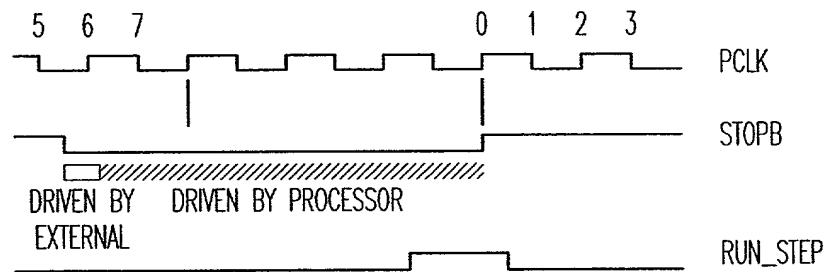


Fig.5D

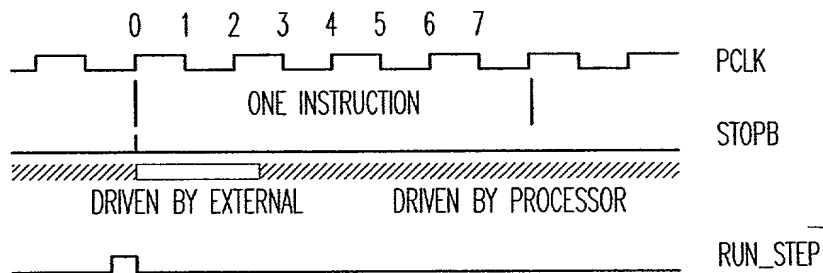


Fig.5E

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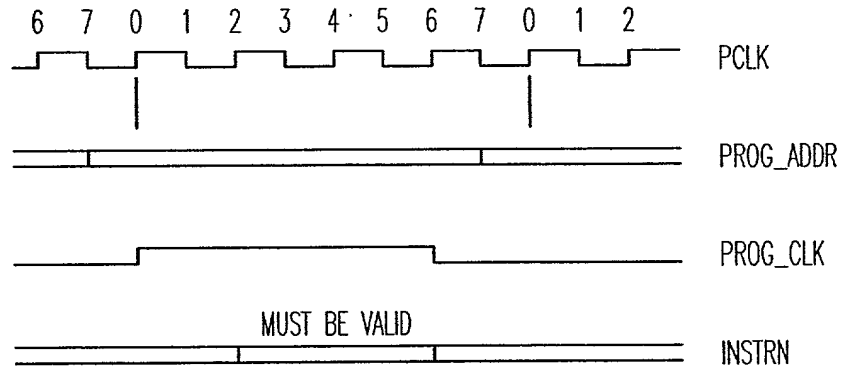


Fig.5F

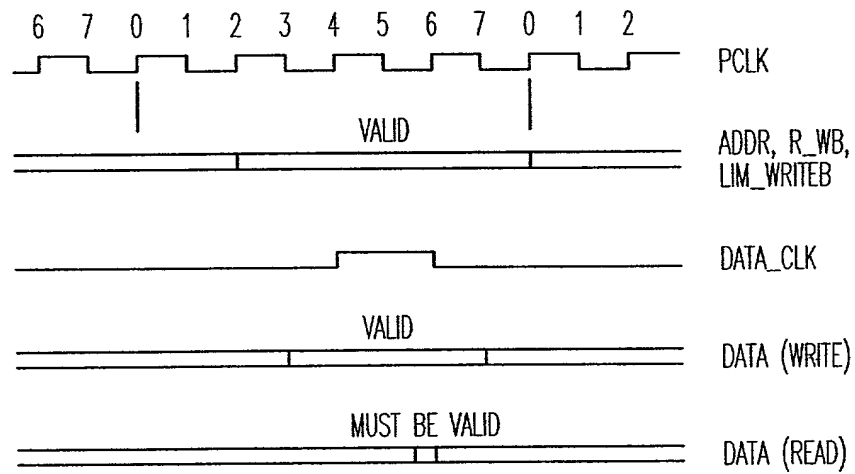


Fig.5G

FIG. 5F

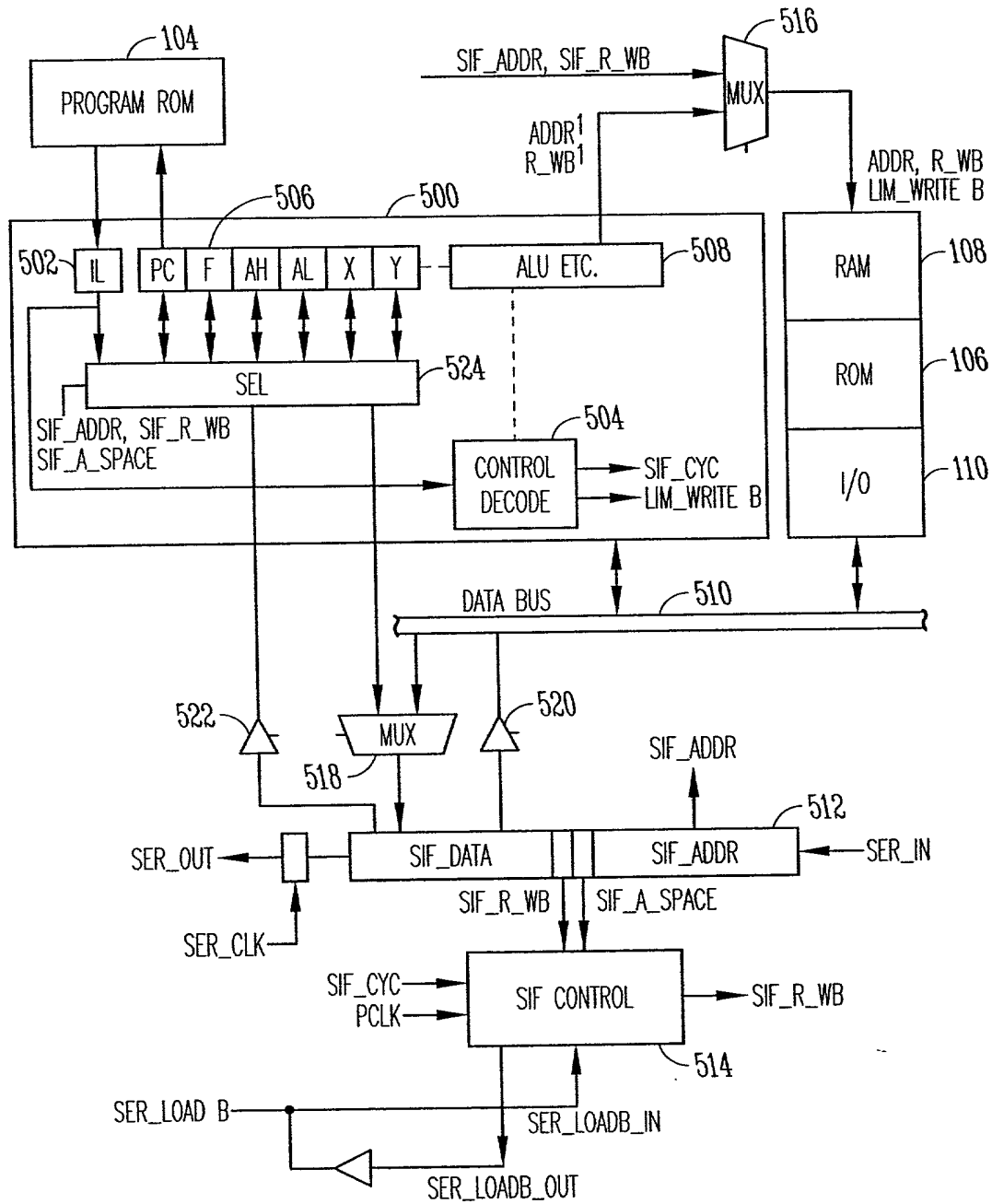


Fig.7

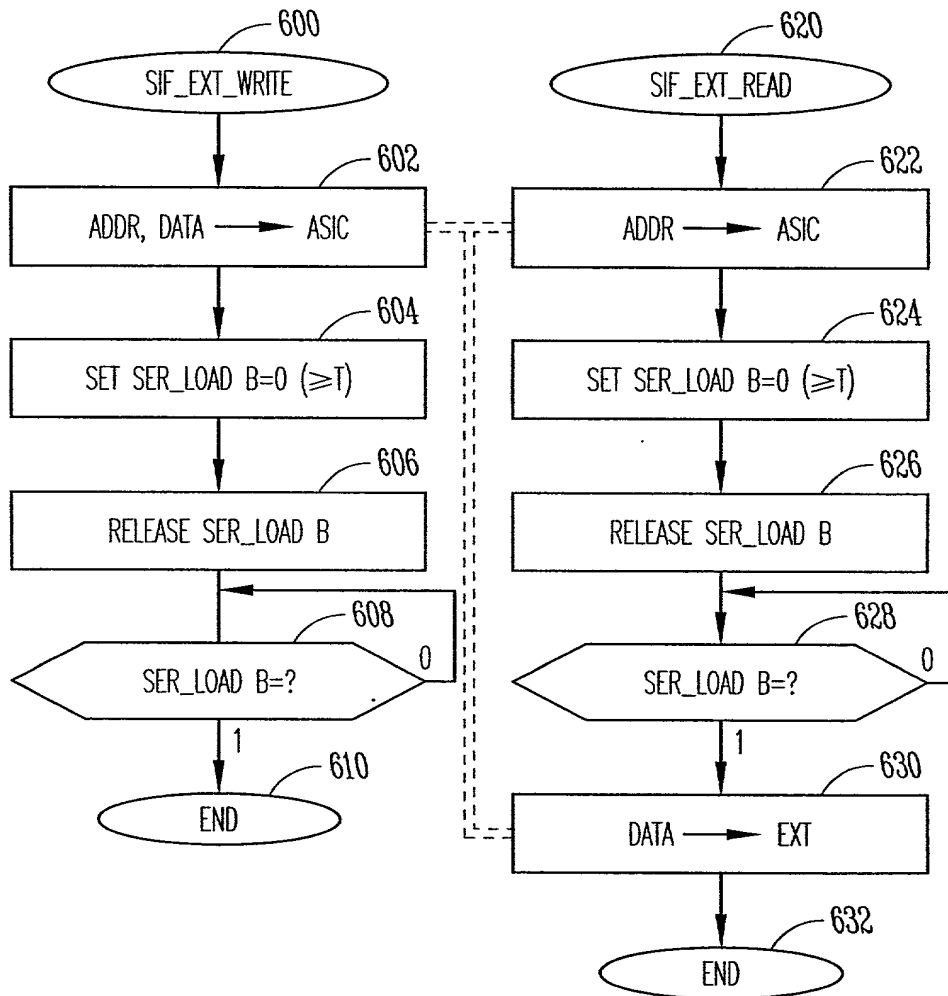


Fig.8

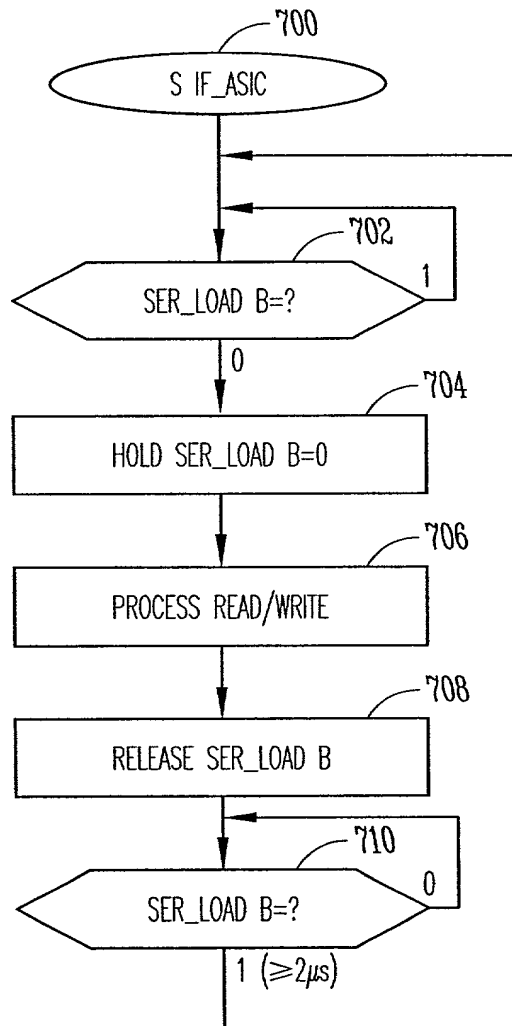


Fig.9

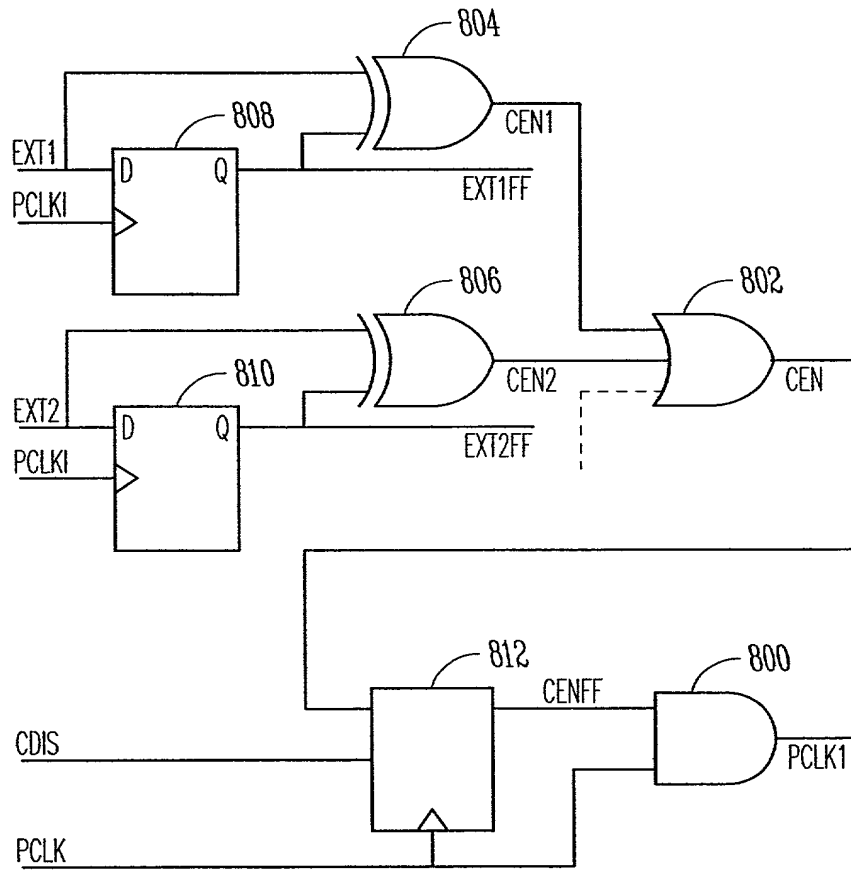


Fig.10

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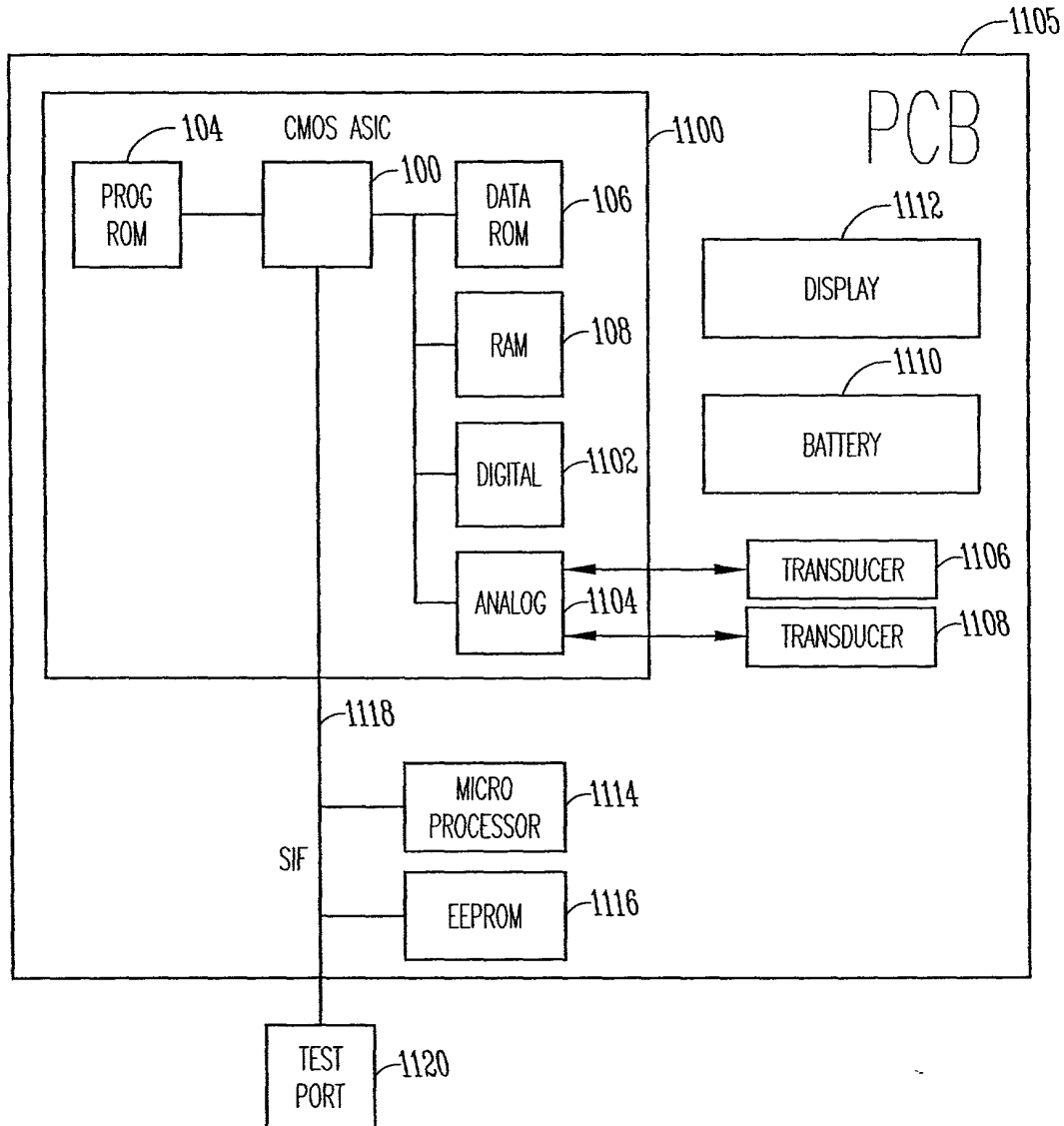


Fig.11

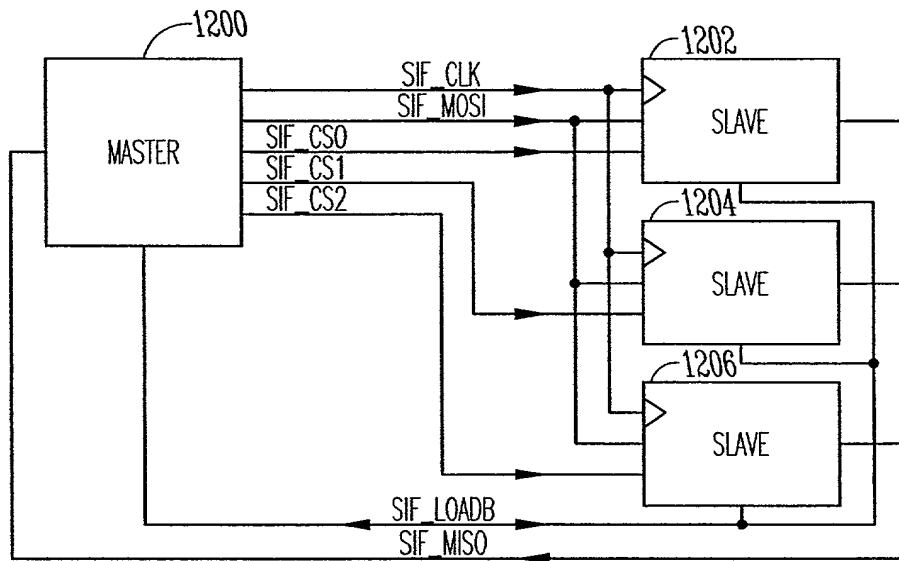


Fig.12